ARM926EJ-S™ Based

32-bit Microprocessor

NUC980 Linux BSP
Revision History

The information described in this document is the exclusive intellectual property of Nuvoton Technology Corporation and shall not be reproduced without permission from Nuvoton.

Nuvoton is providing this document only for reference purposes of NUC980 microprocessor based system design. Nuvoton assumes no responsibility for errors or omissions.

All data and specifications are subject to change without notice.

For additional information or questions, please contact: Nuvoton Technology Corporation.

www.nuvoton.com

Revision 1.06.000 (Released 2025-09-19)

1. USBD: Modified USB gadget driver to pass USB Chapter 9 test.
2. Board Support: Added support for NUC980-IoT-G2 board.
3. USBD: Updated USBD FIFO arrangement and CEP interrupt flow.
4. UART: Fixed bug where RX data collection variable was not independent per port.
5. I2C: Stop I2C controller when transmit timeout occurs.
6. QSPI: Fixed issue where QSPI driver always selected SPI Mode 0.
7. QSPI: Added support for GD5F2GQ5UExxG SPI NAND, Winbond W25N KV/KW/JV/JW SPI NAND, and GigaDevice 4-bit ECC SPI NAND Flash.
8. USBH: Updated driver to support system wakeup by USB remote wakeup.
9. USBD: Fixed issue where a zero-length packet must be sent if transfer length is a multiple of the maximum packet size.
10. NAND: Fixed NAND scan stuck issue.
11. UART: Fixed hard-coded RX buffer size bug.
12. I2C: Added support for configuring I2C hold time and setup time.
13. I2S: Corrected BCLK configuration error.
14. EMAC: Removed redundant IRQ free calls that could cause kernel crashes.

Revision 1.05.000 (Released 2023-03-16)

1. Added pre-build images for IoT, Chili, and Server boards.

Revision 1.04.000 (Released 2022-12-20)

1. Updated NuWriter tool
2. Update Linux Kernel version from 4.4.207 to 4.4.289.
3. Added support for SPI NAND W25N01KV
4. Added support for SPI flash GD25B256E
5. Added support for EMAC VLAN and PTP
6. Updated Crypto AES driver for IPSec application.
7. Minor bug fix.

Revision 1.03.000 (Released 2020-09-08)

1. Updated 64MB and 128MB DDR parameters.
2. Improved NuWriter performance.
3. Added NK-LGNU980 board support.
4. Added NK-RTU980 board support.
5. Added fwupdate sample application.
6. Added new SPI NAND Flash support.
7. Minor bug fix.

Revision 1.02.001 (Released 2019-09-09)

1. Minor bug fix.

Revision 1.02.000 (Released 2019-03-21)

1. Added NK-980IoT board support.
2. Minor bug fix.

Revision 1.01.000 (Released 2019-02-11)

1. Initial release.

Important Notice

Nuvoton Products are neither intended nor warranted for usage in systems or equipment, any malfunction or failure of which may cause loss of human life, bodily injury or severe property damage. Such applications are deemed, "Insecure Usage".

Insecure usage includes, but is not limited to: equipment for surgical implementation, atomic energy control instruments, airplane or spaceship instruments, the control or operation of dynamic, brake or safety systems designed for vehicular use, traffic signal instruments, all types of safety devices, and other applications intended to support or sustain life.

All Insecure Usage shall be made at customer's risk, and in the event that third parties lay claims to Nuvoton as a result of customer's Insecure Usage, customer shall indemnify the damages and liabilities thus incurred by Nuvoton.

Please note that all data and specifications are subject to change without notice.
All the trademarks of products and companies mentioned in this datasheet belong to their respective owners.